



February 20, 2018

## QuickLogic Announces Partnership with Aldec for eFPGA Simulation Flow

- Aldec Active-HDL FPGA Design Creation and Simulation solution, together with QuickLogic Aurora development tools, support complete RTL to simulation to bitstream design flow
- Gives SoC designers a seamless development environment for embedding FPGA logic

SUNNYVALE, Calif., Feb. 20, 2018 /PRNewswire/ -- QuickLogic Corporation (NASDAQ: QUIK), a developer of ultra-low power multi-core voice-enabled SoCs, embedded FPGA IP, display bridge and programmable logic solutions, announced that it has partnered with Aldec, a leading design verification EDA company, to provide seamless simulation support for its [embedded FPGA](#) (eFPGA) technology. Now QuickLogic eFPGA customers can perform design verification using Aldec's industry-proven Active-HDL™ FPGA Design and Simulation software.



Aldec's 34+ years of FPGA/ASIC verification expertise enables QuickLogic's eFPGA customers to work in a seamless environment as part of the Active-HDL tool suite. Active-HDL is a Windows® based integrated FPGA Design Creation and Simulation solution for team-based environments. Active-HDL's Integrated Design Environment (IDE) includes a full HDL and graphical design tool suite and RTL/gate-level mixed-language simulator for rapid deployment and verification of FPGA, and now eFPGA, designs.

QuickLogic's Aurora development tool suite will support easy design migration to and from the Aldec simulation environment for designers who are embedding eFPGA technology into their SoC designs. The combination of the two tool sets will deliver a seamless development environment supporting a simple and complete design flow, from RTL to simulation to bitstream for the embedded FPGA portion of the design.

By embedding eFPGA technology into SoC designs, designers can maintain high performance and low power consumption, and ensure post-manufacturing design flexibility to support design fixes, upgrades, market variants and rapidly evolving standards or market requirements.

"We are very happy to collaborate with Aldec for eFPGA simulation flow support," said Mao Wang, director of product marketing at QuickLogic Corporation. "Simulation is a very important design verification step to ensure proper functionality of the design. Our collaboration with Aldec streamlines this step for our eFPGA customers, enabling them to more quickly and easily complete their embedded FPGA designs and shorten their time-to-market."

"QuickLogic's eFPGA technology is an exciting advancement addressing the re-configurability requirements of today's SoCs. We are looking forward to providing our mutual customers with seamless mixed-language simulation support and a rich-set of debugging environment with our Active-HDL tool suite," said Louie De Luna, director of marketing at Aldec.

The Aldec Active-HDL and QuickLogic Aurora™ development tools supporting QuickLogic's eFPGA technology are both available now from their respective companies.

### About QuickLogic

QuickLogic Corporation (NASDAQ: QUIK) enables OEMs to maximize battery life for highly differentiated, immersive user experiences with Smartphone, Wearable and IoT devices. QuickLogic delivers these benefits through industry leading ultra-low power customer programmable SoC semiconductor solutions, embedded software, and algorithms for always-on voice and sensor processing. The company's embedded FPGA initiative also enables SoC designers to easily implement post production changes, and increase revenue by providing hardware programmability to their end customers. For more information about QuickLogic, visit [www.quicklogic.com](http://www.quicklogic.com).

### About Aldec

Aldec Inc., an industry leader in Electronic Design Verification and offers a patented verification technology tool suite including: RTL Design, RTL Simulation, Hardware-Assisted Verification, SoC/ASIC Emulation & Prototyping, Design Rule Checking, CDC Verification, IP Cores, Requirements Lifecycle Management, DO-254 Functional Verification, Embedded

Solutions, High-Performance Computing and Military/Aerospace solutions. [www.aldec.com](http://www.aldec.com)

*The QuickLogic logo and QuickLogic are registered trademarks of QuickLogic Corporation and Aurora is a trademark of QuickLogic. All other brands or trademarks are the property of their respective holders and should be treated as such.*

Code: QUIK-G

 View original content with multimedia: <http://www.prnewswire.com/news-releases/quicklogic-announces-partnership-with-aldec-for-efpga-simulation-flow-300600938.html>

SOURCE QuickLogic Corporation

News Provided by Acquire Media